

FIG. 4A

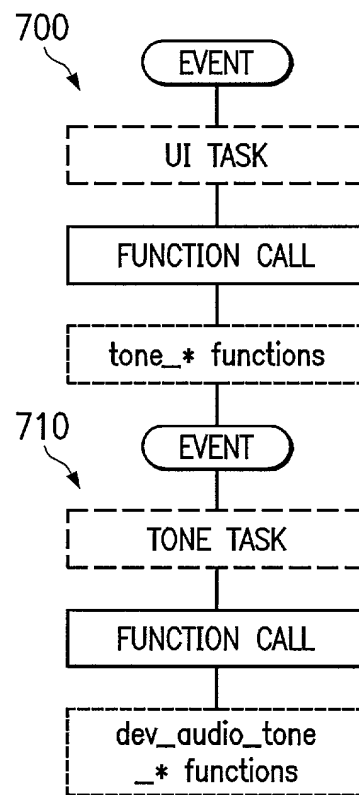


FIG. 7

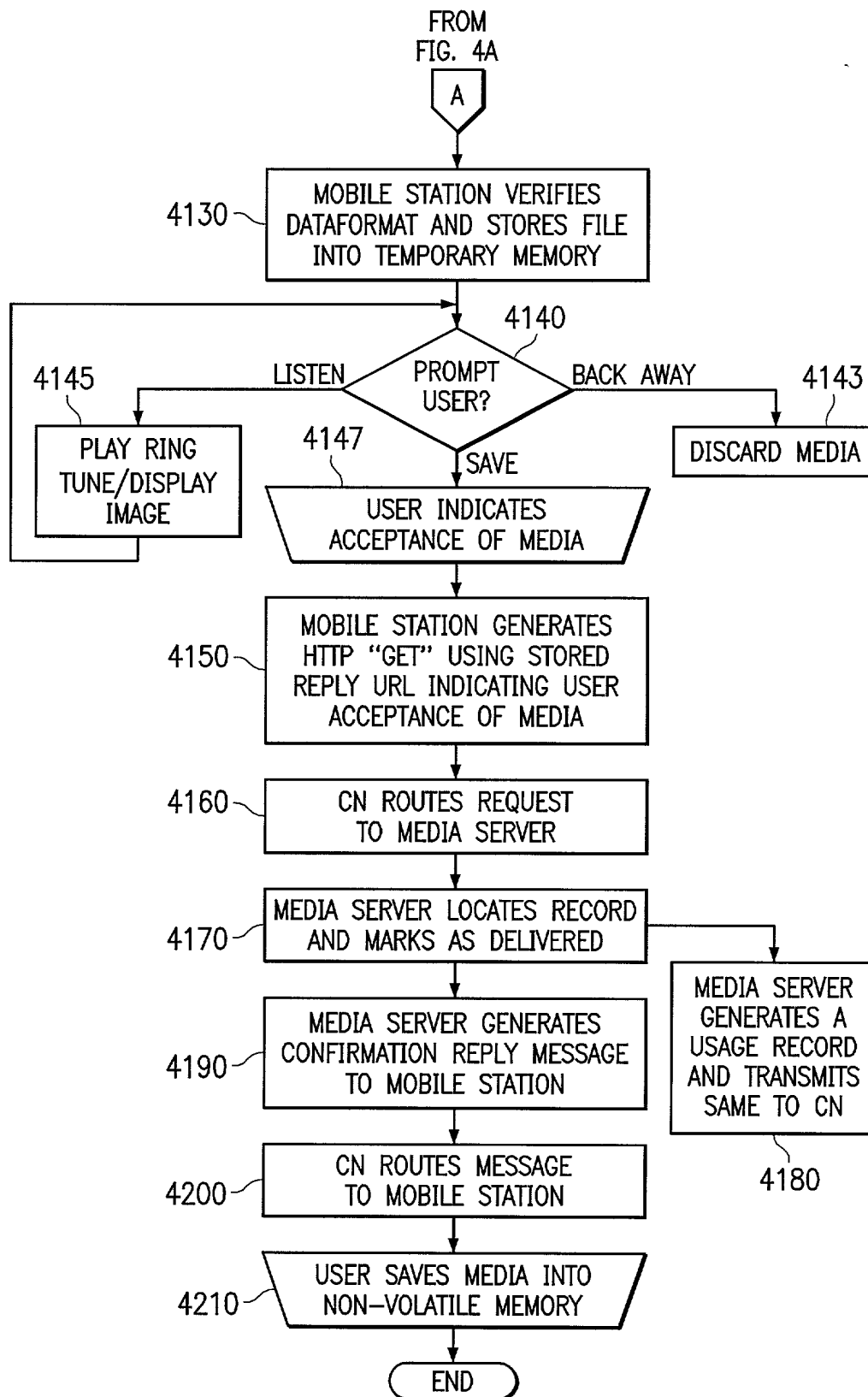


FIG. 4B

FIG. 5 is a block diagram of a system architecture.

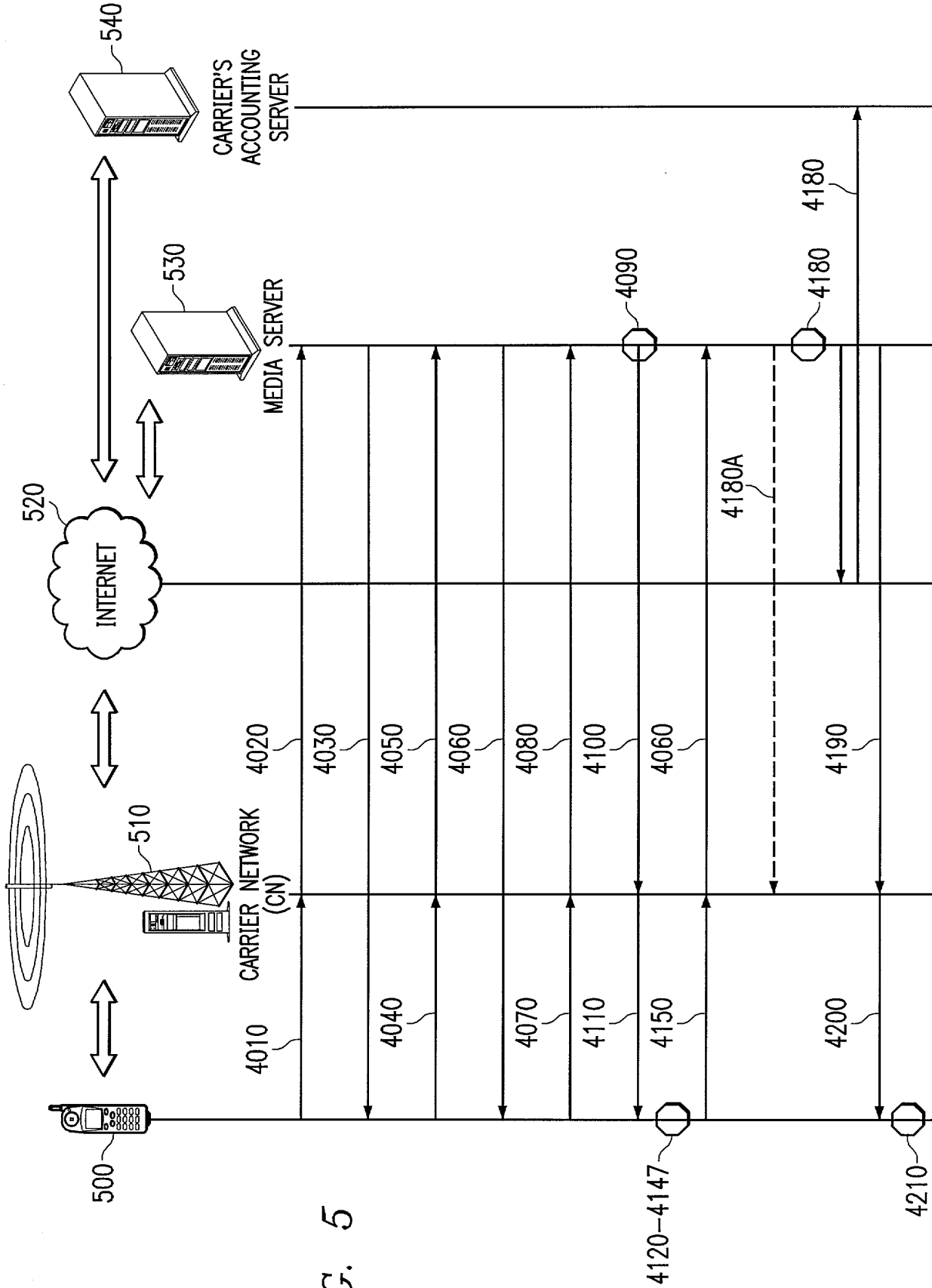


FIG. 5